The Digitrip Family of Low Voltage Electronic Trip Units

		RMS 310		RMS 510	RMS 610	OPTIM 750	RMS 810	RMS 910	OPTIM 1050
									
BREAKER TYPE									
Frame(s)		Series C K-, L-, N-, R-Frames		Series C R-Frame, SPB, DSII, and DSLII	Series C R-Frame, SPB, DSII, and DSLII	Series C L-, N-, R-Frames, SPB, DSII, and DSLII	Series C R-Frame, SPB, DSII, and DSLII	Series C R-Frame, SPB, DSII, and DSLII	Series C L-, N-, R-Frames, SPB, DSII, and DSLII
Ampere Range		70A-2500A		100A-5000A	100A-5000A	100A-5000A	100A-5000A	100A-5000A	100A-5000A
Interrupting Rating @ 480V		35,65, 100 kA		30 thru 200 kA	30 thru 200 kA	35 thru 200 kA	30 thru 200 kA	30 thru 200 kA	35 thru 200 kA
PROTECTION AND COORDINATION									
Protection	Ordering Options	LS, LSG	LSI, LSIG	LI, LS, LSI, LIG LSG, LSIG	LI, LSI, LIG LSG, LSIG	LSI(A), LSIG	LI, LS, LSI, LIG LSG, LSIG	LI, LS, LSI, LIG LSG, LSIG	LSI(A), LISG
	Fixed Rating Plug (I_n)	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes
	Overtemperature Trip	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes
Long Delay	Adjustable Rating Plug (I_n)	Yes	Yes	No	No	No	No	No	No
	Long Delay Setting	0.5-1.0 (I_n)①	0.5-1.0 (I_n)①	0.5-1.0 (I_n)	0.5-1.0 x (I_n)	0.4-1.0 x (I_n)	0.5-1.0 x (I_n)	0.5-1.0 x (I_n)	0.4-1.0 x (I_n)
	Long Delay Time I^2t	12 Seconds	12 Seconds	2-24 Seconds	2-24 Seconds	2-24 Seconds	2-24 Seconds	2-24 Seconds	2-24 Seconds
	Long Delay Thermal Memory	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes
	High Load Alarm	No	No	No	0.85 x I_r	0.5-1.0 x I_r	0.85 x I_r	0.85 x I_r	0.5-1.0 x I_r
Short Delay	Short Delay Setting	200-800% x (I_n)	200-800% x (I_n)	200-600% S1 & S2 x (I_r)	200-600% S1 & S2 x (I_r)	150-800% x (I_r)	200-600% S1 & S2 x (I_r)	200-600% S1 & S2 x (I_r)	150-800% x (I_r)
	Short Delay Time I^2t	100 ms	No	100-500 ms	100-500 ms	100-500 ms	100-500 ms	100-500 ms	100-500 ms
	Short Delay Time Flat	No	I-300 ms	100-500 ms	100-500 ms	100-500 ms	100-500 ms	100-500 ms	100-500 ms
	Short Delay Time ZSI	No	No	Yes	Yes	Yes	Yes	Yes	Yes
Instantaneous	Instantaneous Setting	No	200-800% x (I_n)	200-600% M1 & M2 x (I_n)	200-600% M1 & M2 x (I_n)	200-800% x (I_n)	200-600% M1 & M2 x (I_n)	200-600% M1 & M2 x (I_n)	200-800% x (I_n)
	Discriminator	No	No	Yes√	Yes√	Yes	Yes√	Yes√	Yes
	Instantaneous Override	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes
Ground Fault	Ground Fault Setting	Var/Frame③	Var/Frame③	25-100% x (I_n)③	25-100% x (I_n)③	20/25-100%③	25-100% x (I_n)③	25-100% x (I_n)③	20/25-100%③
	Ground Fault Delay I^2t	No	No	100-500 ms	100-500 ms	100-500 ms	100-500 ms	100-500 ms	100-500 ms
	Ground Fault Delay Flat	I-500 ms	I-500 ms	100-500 ms	100-500 ms	100-500 ms	100-500 ms	100-500 ms	100-500 ms
	Ground Fault ZSI	No	No	Yes	Yes	Yes	Yes	Yes	Yes
	Ground Fault Thermal Memory	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes
SYSTEM DIAGNOSTICS									
Cause of Trip LEDs		No	No	Yes	Yes	Yes	Yes	Yes	Yes
Magnitude of Trip Information		No	No	No	Yes	Yes	Yes	Yes	Yes
Remote Signal Contacts		No	No	No	Yes	Yes	Yes	Yes	Yes
SYSTEM MONITORING									
Digital Display		No	No	No	Yes	Yes≈	Yes	Yes	Yes≈
Current		No	No	No	Yes	Yes	Yes	Yes	Yes
Voltage		No	No	No	No	No	No	Yes	Yes
Power and Energy		No②	No②	No②	No②	No	Yes	Yes	Yes
Power Quality – Harmonics		No	No	No	No	No	No	Yes	Yes
Power Factor		No	No	No	No	No	Yes⑤	Yes	Yes
SYSTEM COMMUNICATIONS									
IMPACC		No	No	No	No	Yes	Yes	Yes	Yes
FIELD TESTING									
Testing Method①		Test Set	Test Set	Integral	Integral	OPTIMizer, BIM, IMPACC	Integral	Integral	OPTIMizer, BIM, IMPACC

① Set by adjustable rating plug.
② Yes, with addition of IQ Energy Sentinel.
③ Not to exceed 1200A.
√ LS, LSG only.
⑤ Over IMPACC only.
≈ By OPTIMizer/BIM.
I_n = Rating plug rating.
I_r = LDPU setting.
I_s = Sensor rating.
BIM = Breaker Interface Module.